



TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

14

Application No.

09/989,736

Filing Date

November 19, 2001

First Named Inventor

Alexander D. Peleg

Art Unit

2121

Examiner Name

E. Moise

Attorney Docket Number

42390P2445C3

ENCLOSURES (check all that apply)

☒ Fee Transmittal Form

☐ Fee Attached

☐ Amendment / Response

☐ After Final

☐ Affidavits/declaration(s)

☐ Extension of Time Request

☐ Express Abandonment Request

☒ Information Disclosure Statement

☒ PTO/SB/08

☐ Certified Copy of Priority Document(s)

☐ Response to Missing Parts/Incomplete Application

☐ Basic Filing Fee

☐ Declaration/POA

☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

☐ Drawing(s)

☐ Licensing-related Papers

☐ Petition

☐ Petition to Convert a Provisional Application

☐ Power of Attorney, Revocation Change of Correspondence Address

☐ Terminal Disclaimer

☐ Request for Refund

☐ CD, Number of CD(s)

☐ After Allowance Communication to Group

☐ Appeal Communication to Board of Appeals and Interferences

☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)

☐ Proprietary Information

☐ Status Letter

☒ Other Enclosure(s) (please identify below):

- First Class Certificate of Mailing;
- stamped return receipt postcard;
and
- copies of fifty-one (51) cited references

Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name

Brent E. Vecchia, Reg. No. 48,011

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Signature

Brent E. Vecchia

Date

8-19-05

CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Krista Mathieson

Signature

Krista Mathieson

Date

Aug. 19, 2005



1fw

FEE TRANSMITTAL for FY 2005

Patent fees are subject to annual revision.

Complete if Known

Application Number 09/989,736
Filing Date November 19, 2001
First Named Inventor Alexander D. Peleg
Examiner Name E. Moise
Art Unit 2121
Attorney Docket No. 42390P2445C3

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$) 0.00

METHOD OF PAYMENT (check all that apply)

☒ Check ☐ Credit card ☐ Money Order ☐ None ☐ Other (please identify):

☒ Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☐ Charge fee(s) indicated below, except for the filing fee

☒ Charge any additional fee(s) or underpayment of fee(s) under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20. ☒ Credit any overpayments

FEE CALCULATION

1. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
20*	0	50.00	\$0.00
3*	0	200.00	\$0.00

Multiple Dependent

The PTO did not receive the following listed item(s) check

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
1202	50	2202	25	Claims in excess of 20
1201	200	2201	100	Independent claims in excess of 3
1203	360	2203	180	Multiple Dependent claim, if not paid
1204	300	2204	150	**Reissue independent claims over original patent
1205	300	2205	150	**Reissue claims in excess of 20 and over original patent
SUBTOTAL (1)		(\$)		0.00

**or number previously paid, if greater. For Reissues, see below

2. ADDITIONAL FEES

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet.	
2053	130	2053	130	Non-English specification	
1251	120	2251	60	Extension for reply within first month	
1252	450	2252	225	Extension for reply within second month	
1253	1,020	2253	510	Extension for reply within third month	
1254	1,590	2254	795	Extension for reply within fourth month	
1255	2,160	2255	1,080	Extension for reply within fifth month	
1401	500	2401	250	Notice of Appeal	
1402	500	2402	250	Filing a brief in support of an appeal	
1403	1,000	2403	500	Request for oral hearing	
1451	1,510	2451	1,510	Petition to institute a public use proceeding	
1460	130	2460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	
1809	790	1809	395	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	790	2810	395	For each additional invention to be examined (37 CFR § 1.129(b))	
Other fee (specify)					
SUBTOTAL (2)		(\$)			

SUBMITTED BY

Complete (if applicable)

Name (Print/Type) Brent E. Vecchia Registration No. 48,011 Telephone (303) 740-1980
Signature [Signature] Date 8-19-05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In Re the Application of:

ALEXANDER D. PELEG, ET AL.

Application No.: 09/989,736

Filed: November 19, 2001

For: **Method and Apparatus for Performing
Multiply-Add Operations on Packed Data**

Art Group: 2121

Examiner: E. Moise

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 8-19-05

Brent E. Vecchia
Brent E. Vecchia, Reg. No. 48,011

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Krista Mathieson on Aug. 19, 2005
Krista Mathieson Date



US Department of Commerce
Patent and Trademark Office

Form PTO-1449 (Modified)

Atty. Docket No.:
42390P2445C3

Application No.:
09/989,736

Applicant:
Alexander D. Peleg

Filing Date:
November 19, 2001

US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
	02/15/72	3,641,736	Dwyer			
	08/19/75	3,900,724	McIver			
	03/02/76	3,941,990	Rabasse et al.			
	09/17/85	4,542,476	Nagafuji, et al.			
	06/30/87	4,677,582	Nagafuji			
	07/26/88	4,760,545	Inagami et al.			
	03/07/89	4,811,269	Hirose et al.			
	12/26/89	4,890,218	Bram			
	07/31/90	4,945,479	Rusterholz et al.			
	11/20/90	4,972,362	Elkind et al.			
	01/15/91	4,985,848	Pfeiffer et al.			
	03/19/91	5,001,662	Baum			
	09/10/91	5,047,975	Patti et al.			
	01/14/92	5,081,698	Kohn			
	06/30/92	5,126,964	Zurawski			
	11/30/92	5,161,247	Murakami et al.			
	02/23/93	5,189,636	Patti et al.			
	05/11/93	5,210,711	Rossmere, et al.			
	08/31/93	5,241,493	Chu et al.			
	04/12/94	5,303,355	Gergen et al.			
	05/10/94	5,311,508	Buda, et al.			
	07/05/94	5,327,369	Ashkenazi			

Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner	Date Considered
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Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)			Atty. Docket No.: 42390P2445C3		Application No.: 09/989,736		
			Applicant: Alexander D. Peleg				
			Filing Date: November 19, 2001				
US Patent Documents							
Examiner's Initials		Date	Document Number	Name	Class	Sub- Class	Filing Date
		02/14/95	5,390,135	Lee et al.			
		06/20/95	5,426,598	Hagihara			
		05/07/96	5,515,520	Hatta et al.			
		11/26/96	5,579,253	Lee et al.			
		12/31/96	5,590,365	Ide et al.			
		02/25/97	5,606,677	Balmer et al.			
		06/24/97	5,642,306	Mennemeir			
		09/30/97	5,673,427	Brown et al.			
		12/23/97	5,701,508	Glew et al.			
		04/21/98	5,742,840	Hansen et al.			
		06/09/98	5,765,037	Morrison et al.			
		07/07/98	5,778,419	Hansen et al.			
		08/11/98	5,793,661	Dulong, et al.			
		08/11/98	5,794,060	Hansen et al.			
		08/11/98	5,794,061	Hansen et al.			
		09/01/98	5,802,336	Peleg et al.			
		09/08/98	5,806,049	Petruzzi			
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
Examiner				Date Considered			

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				Applicant: Alexander D. Peleg			
				Filing Date: November 19, 2001			
US Patent Documents							
		09/15/98	5,809,321	Hansen et al.			
		10/06/98	5,819,101	Peleg et al.			
		10/13/98	5,822,603	Hansen et al.			
		11/10/98	5,835,392	Dulong et al.			
		12/22/98	5,852,726	Lin et al.			
		02/09/99	5,870,619	Wilkinson et al.			
		03/09/99	5,880,983	Elliot et al.			
		03/16/99	5,884,071	Kosaraju			
		03/16/99	5,883,824	Lee et al.			
		08/10/99	5,936,872	Fischer, et al.			
		09/14/99	5,953,241	Hansen et al.			
		11/09/99	5,983,257	Dulong et al.			
		12/21/99	6,006,318	Hansen et al.			
		01/25/00	6,018,351	Mennemeir, et al.			
		03/07/00	6,035,387	Hsu et al.			
		05/21/00	6,041,403	Parker, et al.			
		09/19/00	6,122,725	Roussel et al.			
		11/28/00	6,154,831	Thayer et al.			
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
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				Applicant: Alexander D. Peleg			
				Filing Date: November 19, 2001			
US Patent Documents							
		01/23/01	6,178,482	Sollars			
		02/20/01	6,192,467	Abdallah et al.			
		05/08/01	6,230,253	Roussel et al.			
		05/08/01	6,230,257	Roussel et al.			
		05/15/01	6,233,671	Abdallah et al.			
		07/10/01	6,260,137	Fleck, et al.			
		09/25/01	6,295,599	Hansen et al.			
		04/23/02	6,378,060	Hansen et al.			
		05/07/02	6,385,634	Peleg, et al.			
		07/04/02	3,675,001	Singh			
		10/22/02	6,470,370	Fischer, et al.			
		12/31/02	6,502,117	Golliver et al.			
		06/24/03	6,584,482	Hansen et al.			
		11/04/03	6,643,765	Hansen et al.			
		04/20/04	6,725,356	Hansen et al.			
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
		4/23/1997	DE 3636106	Germany			
		10/14/1999	DE 19914617	Germany			
		10/7/1999	WO 99/50740	PCT			
		7/3/1997	WO 97/23821	PCT			
		12/01/00	GB 2339040	Great Britian			
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
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			Applicant: Alexander D. Peleg				
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Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
		06/29/97	WO 97/22923	PCT			
		06/29/97	WO 97/22924	PCT			
		06/27/97	WO 97/22921	PCT			
		03/06/97	WO 97/08608	PCT			
		03/19/90	JP 3-268024	Japan			
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
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US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)		Atty. Docket No.: 42P390P2445C3		Application No.: 09/989,736			
		Applicant: Aleaxander D. Peleg					
		Filing Date: November 19, 2001					
US Patent Documents							
Examiner's Initials		Date	Document Number	Name	Class	Sub- Class	Filing Date
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
		Samsung Electronics, "21164 Alpha Microprocessor Data Sheet", 1997.					
		Silicon Graphics, "Silicon Graphics Introduces Enhanced MIPS Architecture to Lead the Interactive Digital Revolution", 10/21/96, 2 pages.					
		Philips Electronics, "TriMedia TM1000 Preliminary Data Book", 1997.					
		"MIPS Digital Media Extension", pp. C1 to C40.					
		"MIPS Extension for Digital Media with 3D", MIPS Technologies, Inc. March 12, 1997, pp. 1-26.					
		"The VIS™ Instruction Set", Sun Microsystems, Inc. 1997, pp. 1-2.					
		Abbott et al., "Broadband Algorithms with the MicroUnity Media processor", Proceedings of COMPCON '96, 1996, pages 349-354					
		Hayes, et al., "MicroUnity Software Development Environment", Proceedings of COMPCOM '96, 1996, pages 341-348.					
		"Silicon Graphics Introduces Compact MIPS@ RISC Microprocessor Code for High Performance at a Low Cost", Oct. 21, 1996, downloaded from the Website webmaster@www.sgi.com, pp. 1-2					
		Killian, Earl, "MIPS Extension for Digital Media", Silicon Graphics, pp. 1-10					
		"MIPS V Instruction Set", pp. B1 to B7.					
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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
		Santoro, Mark; Design and Clocking of VLSI Multipliers. Technical Report No. CSL -TR-89-397, October 1989. Pages i-xii and 1-118.					
		Santoro, Mark, et al.; SPIM: A Pipelined 64X64 bit Iterative Multiplier, IEEE Journal of Solid -State Circuits, Vol. 24, No. 2, April 1989. Pages 487-493.					
		Santoro, Mark, et al.; SESSION II: HIGH-SPEED MICROPROCESSOR. WAM 2.6: A Pipelined 64X64b Iterative Array Multiplier. 1988 IEEE International Solid State Circuits Conference . Pages 36-37 and 290.					
		BIT Preliminary, Bipolar Integrated Technology, Inc. B3110/B3120;B2110/B2120 Floating Point Chip Set. Pages 1-40.					
		Eklind, Bob, et al. A SUB 10 nS Bipolar 64 Bit Integrated/Floating Point Processor Implemented On Two Circuits. IEEE 1987 BCTM, pages 101- 104					
		Leibowitz, Bob; et al., System Design; ECL gains ground in battle against CMOS. Computer Design; April 1, 1987. Pages 91-95.					
		IBM. TBD: Double Speed, Single Precision Vector Register Organization Using Double Port Chips. Feb. 1981. Pp.1-6.					
		Farnwald, P. Michael; High Bandwidth Evaluation of Elementary Functions, S-1 Project. IEEE 1981. Pages 139 - 142.					
		Farnwald, P. Michael; On the Design of High Performance Digital Arithmetic Units. UCRL-53190. August 1981. Pages i-vii and 1-95.					
		Grimes et al.; 64-Bit Processor. The Intel i860 64-Bit Processor: A General-Purpose CPU with 3D Graphics Capabilities. July 1989. Pages 85-94.					
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		Ide, et al.; A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors. IEEE Journal of Solid State Circuits, Vol. 28, No. 3, March 1993. Pages 352 - 361.					
		Lino, et al.; ISSCC 92 SESSION 6/ MICROPROCESSORS/ TA 6.5: A 289MFLOPS Single-Chip Supercomputer. 1992 IEEE International Solid State Circuits Conference. Pages 112-113.					
		Kohn, et al.; Introducing the Intel i860 64 Bit Microprocessor, Intel Corp. August 1989. Pages 15 - 30.					
		Lee, Ruby B.; Accelerating Multimedia with Enhanced Microprocessors. Hewlett-Packard. IEEE Micro, April 1995. Pages 22-32.					
		Lee, Ruby B.; Realtime MPEG Video via Software Decompression on a PA-RISC Processor. 1995 IEEE, pages 186-192.					
		Manferdelli, John L. et al.; Signal Processing Aspects of the S-1 Multiprocessor Project. UCRL-84658. July 28, 1980. Pages 1-8.					
		Spaderna, D., et al.; An Integrated Floating Point Vector Processor for DSP and Scientific Computing". SHARP Microelectronics Technology Inc. IEEE 1989. Pages 8-13.					
		Undy, Steve, et al.; A Low-Cost Graphics and Multimedia Workstation Chip Set. April 1994, IEEE micro, pages 10 - 22.					
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		"64-Bit and Multimedia Extensions In the PA-RISC 2.0 Architecture", Hewlett Packard, downloaded from the Website rblee@cup.hp.com.huck@cup.hp.com, pp. 1-8.					
		"ULTRASPARCH TM The Visual Instruction Set (VIS TM): On Chip Support for New-Media Processing", Sun Microsystems, Inc. 1996, pp. 1-7.					
		"ULTRASPARCH TM and New Media Support Real-Time MPEG2 Decode with the Visual Instruction Set (VIS TM), Sun Microsystems, 1996, pp. 1-8.					
		ULTRASPARCH TM Ultra Port Architecture (UPA): The New-Media System Architecture, Sun Microsystems, Inc. 1996, pp.1-4.					
		ULTRASPARCH TM Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pp. 1-5.					
		"The ULTRASPARCH Processor - Technology White Paper, Sun Microsystems, Inc. 1995, 37 pages.					
		"AMD-3D TM Technology Manual, Advanced Micro Devices, February 1998.					
		Hansen, Craig, "Architecture of a Broadband Mediaprocessor, MicroUnity Systems Engineering, Inc., 1996, pp. 334-354.					
		Levinthal, Adam, et al. "Parallel Computers for Graphics Applications, Pixar, San Rafael, CA, 1987, pp. 193-198.					
		Levinthal, Adam; Porter, Thomas, "Chap-A SIMD Graphics Processor", Computer Graphics Project, Lucasfilm Ltd., 1984, pp. 77-82.					
		Wang, Mangaser, Shrinivan, "A procesor Architecture for 3D Graphics Calculations, Computer Motion, Inc., pp. 1-23.					
		Visual Instruction Set (VIS TM), User's Guide, Sun Microsystems, Inc., verison 1.1 March, 1997.					
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